R E M A R K S

Claims 1-11 are pending in the application. Applicants amend claims 4 and 6 for further clarification. No new matter has been added.

Applicants respectfully request that the Examiner acknowledge receipt of all certified copies of the priority documents for this application.

Claims 4-8 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent Application Publication No. 2003/0152152 to Dunne et al. Applicants respectfully traverse the rejection.

The Examiner relied upon the description of a mode detector 15, an analyzer 30, and respective processors with different types of "enhancement" in Dunne et al. as alleged disclosure of the claimed input data detection, and first and third data features, respectively.

Applicants respectfully point out to the Examiner that the cited portions of Dunne et al. only include description of a mode detector 15 and an analyzer 30 that, together, signal corresponding processors with instructions for processing data, which is separately fed to the processors from a received signal, with types of "enhancement" according to a type of the received signal. And the mode detector 15 and analyzer 30 described in Dunne et al.—and relied upon by the Examiner—do not themselves process first and third input data to output fourth data—i.e., a processor signal is not incorporated in processed data. Thus, Dunne et al., as cited and relied upon by the Examiner, fail to disclose adding a reset signal to processed output data.

In other words, Dunne et al., as cited and relied upon by the Examiner, fail to disclose,

> "[a] data processing method for inputting data, the input
> data including one of a first data and a third data, the first data
> formed by encoding a signal with a first encoding system, and
> the third data formed by multiplexing second data formed by
> encoding the signal with a second encoding system and said

first data, the data processing method <u>outputting fourth data</u>
<u>incorporating at least a part of a respective one of the first and</u>
<u>third data</u>, the data processing method comprising the steps of:
　　　　detecting if the input data is the first data or the third
data; and
　　　　determining whether to transition from a first operation
mode to a second operation mode for coding the input data,
wherein
　　　　when an operation mode is to be switched to said first
mode or said second mode, <u>a signal for resetting a data</u>
<u>processor for decoding the data output with said second</u>
<u>encoding system is</u> ***added***<u>, before such switching operation,</u> ***to***
said fourth data and is then outputted," as recited in claim 4.
(Emphasis added)

Accordingly, Applicants respectfully submit that claim 4 is patentable over <u>Dunne et</u>

<u>al.</u> for at least the foregoing reasons. Claims 6 and 8 incorporate features that correspond to

those of claim 4 cited above, and are, therefore, patentable over <u>Dunne et al.</u> for at least the

same reasons.

Correspondingly, the Examiner only cited description of a multiplexer 70 in <u>Dunne et</u>

<u>al.</u> as alleged disclosure of the claimed "first mode and third data" features recited in claim 5.

The cited portions of <u>Dunne et al.</u>, again, only include description of respective enhancements

by processors that are performed according to a detected input signal, and such portions of

<u>Dunne et al.</u> do not include any disclosure of the claimed data replacement features.

In other words, <u>Dunne et al.</u>, as cited and relied upon by the Examiner, do not

disclose,

　　　　"[a] data transmission system communicating between
a first terminal transmitting second data formed by a second
encoding system, and a second terminal for receiving
information transmitted from the first terminal comprising;
　　　　a first data terminal for inputting said second data and
outputting first data encoded with a first encoding system in a
first mode and third data multiplexing said second data and
said first data in a second mode; and
　　　　a second data terminal for inputting said first or third
data output and outputting to the second terminal, in the first
mode, fifth data formed by encoding said first data input with a

second encoding system and also outputting, in the second
mode, said second data isolated from said third data, wherein
<u>when said second data terminal is in said first mode and</u>
<u>said third data is input, a part of said third data where said</u>
<u>second data is multiplexed *is replaced with the particular data*</u>
<u>and said particular data is outputted through the encoding</u>
<u>thereof with said second encoding system</u>," as recited in claim
5. (Emphasis added)

Accordingly, Applicants respectfully submit that claim 5 is patentable over <u>Dunne et</u>
<u>al.</u> for at least the foregoing reasons. Claim 7 incorporates features that correspond to those
of claim 5 cited above, and is, therefore, patentable over <u>Dunne et al.</u> for at least the same
reasons.

Claims 1-3 and 9-11 stand rejected under 35 U.S.C. § 103(a) as being unpatentable
over <u>Dunne et al.</u> in view of U.S. Patent Application Publication No. 2004/0120302 to <u>Sebire</u>
<u>et al.</u> Applicants respectfully traverse the rejection.

The Examiner relied upon a description of a "third mode" in <u>Sebire et al.</u> as alleged
suggestion of the claimed third mode features. The cited portion of <u>Sebire et al.</u> merely
includes description of a technique for including <u>*a*</u> "third mode," which third mode does not
correspond to any features of the claimed third mode. As such, the addition of this reference
would still have failed to cure the above-described deficiencies of <u>Dunne et al.</u> with respect to
claims 4-8, even assuming, <u>arguendo</u>, that such an addition would have been obvious to one
skilled in the art. Accordingly, Applicants respectfully submit that claims 9-11, which
depend from claims 6 and 8, respectively, are patentable over the cited references for at least
the foregoing reasons.

Claim 1 incorporates features that correspond to those of claim 5 cited above, and is,
therefore, together with claims 2-3 dependent therefrom, patentable over the cited references
for at least the foregoing reasons.

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In view of the remarks set forth above, this application is in condition for allowance

which action is respectfully requested. However, if for any reason the Examiner should

consider this application not to be in condition for allowance, the Examiner is respectfully

requested to telephone the undersigned attorney at the number listed below prior to issuing a

further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,

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